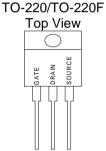


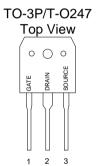
GENERAL DESCRIPTION

虹冠電

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

PIN CONFIGURATION



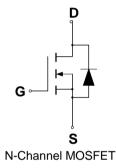


ABSOLUTE MAXIMUM RATINGS

FEATURES

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I_{DSS} and V_{DS}(on) Specified at Elevated Temperature ٠
 - Isolated Mounting Hole Reduces Mounting Hardware

SYMBOL



Rating		Value	Unit
Drain to Current – Continuous	I _{D (1)}	28	А
 Pulsed 	I _{DM}	84	
Gate-to-Source Voltage – Continue	V _{GS}	±20	V
Total Power Dissipation – TO220	PD	245	W
– TO220FP		42	
-TO3P		255	
-TO247		227	W/°C
Derate above 25℃ – TO220		1.96	
– TO220FP		0.33	
-TO3P		2.04	
-TO247		1.82	
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $- T_J = 25^{\circ}C$		320	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_{L} = 8A, L = 10mH, R_{G} = 25)$			
Thermal Resistance – Junction to Case -TO220	JC	0.51	°C/W
 Junction to Case -TO220FP 		3	
 Junction to Case -TO3P 		0.49	
 Junction to Case -TO247 		0.55	
 Junction to Ambient -TO220, TO220FP 	JA	62.5	
 Junction to Ambient -TO3P ,TO247 		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C
(1) Drain current limited by maximum junction temperature	-		L Ŭ

current limited by maximum junction temperature

ORDERING INFORMATION

Part Number	TOP MARK	Part Number	Packing Mthod	Note
GP28S50XN220 (Note1)	GP28S50X	TO-220	Tube	
GP28S50XN220FP (Notte1)	GP28S50X	TO-220FP	Tube	
GP28S50XN3P (Notte2)	GP28S50X	TO-3P	Tube	
GP28S50XN247 (Notte2)	GP28S50X	TO-247	Tube	
GP28S50GN220 (Note2)	GP28S50G	TO-220	Tube	
GP28S50GN220FP (Notte2)	GP28S50G	TO-220FP	Tube	
GP28S50GN3P (Notte2)	GP28S50G	TO-3P	Tube	
GP28S50GN247 (Notte2)	GP28S50G	TO-247	Tube	

Note1: X : Suffix for Halogen Free Product, **Note2:** G : Suffix for PB Free Product,

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, T_{J} = 25 $^{\circ}\mathrm{C}$.

			GP28S50			
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V _{(BR)DSS}	500			v
(V _{GS} = 0 V, I _D = 250 μA)		V (BR)DSS	500			v
Drain-Source Leakage Current		I _{DSS}			1	uA
$(V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-For	I _{GSSF}			100	nA	
$(V_{gsf} = 20 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Rev	ent-Reverse				100	nA
(V _{gsr} = - 20 V, V _{DS} = 0 V)						
Gate Threshold Voltage		V _{GS(th)}	2	3	4	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$		00(11)			-	
Static Drain-Source On-Resistance ($V_{GS} = 10 \text{ V}, I_D = 15\text{ A}$) *		R _{DS(on)}			125	m
Gate resistance (f=1MHz, open drain)		RG		2.7		
Input Capacitance		C _{iss}		1517.7		pF
Output Capacitance	$(V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz)$	C _{oss}		1766.7		pF
Reverse Transfer Capacitance		C _{rss}		50.3		pF
Turn-On Delay Time	$(V_{DD} = 250 \text{ V}, I_D = 20 \text{ A}, R_G = 25)^*$	t _{d(on)}		34.9		ns
Rise Time		tr		104.5		ns
Turn-Off Delay Time		t _{d(off)}		97.4		ns
Fall Time		t _f		65.0		ns
Total Gate Charge	$(V_{DS} = 400 \text{ V}, I_D = 20 \text{ A}, V_{GS} = 10 \text{ V})^*$	Qg		40.7		nC
Gate-Source Charge		Q _{gs}		10.1		nC
Gate-Drain Charge		Q _{gd}		18.7		nC
	SOURCE-DRAIN DIODE CH	ARACTERISTICS				
Forward On-Voltage(1)	$(I_{S} = 20 \text{ A}, d_{IS}/d_{I} = 100 \text{ A}/\mu\text{s})$	V _{SD}			1.5	V
Forward Turn-On Time		t _{on}		**		ns
Reverse Recovery Time		t _{rr}		741		ns

* Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%

** Negligible, Dominated by circuit inductance



TYPICAL ELECTRICAL CHARACTERISTICS

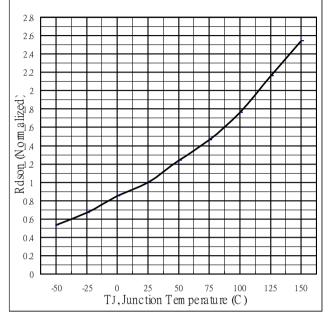


Fig 1. On-Resistance Variation with vs. Temperature

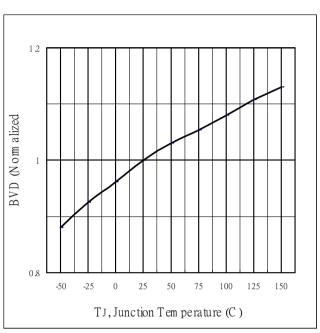


Fig.2 Breakdown Voltage Variation vs. Temperature

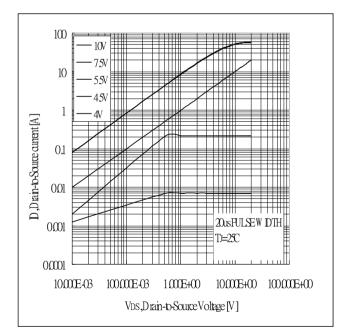


Fig 3. Typical Output Characteristics

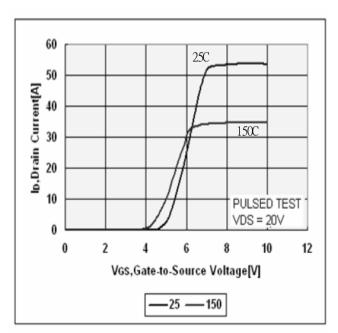


Fig 4. Typical Transfer Characteristics



GP28S50 Power Field Effect Transistor

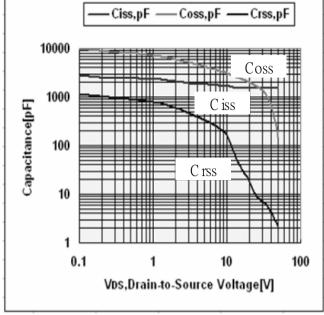
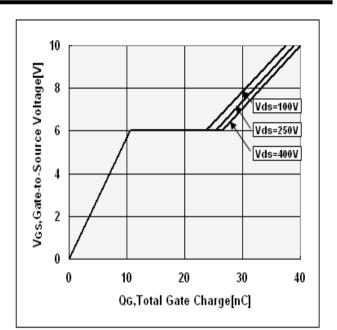


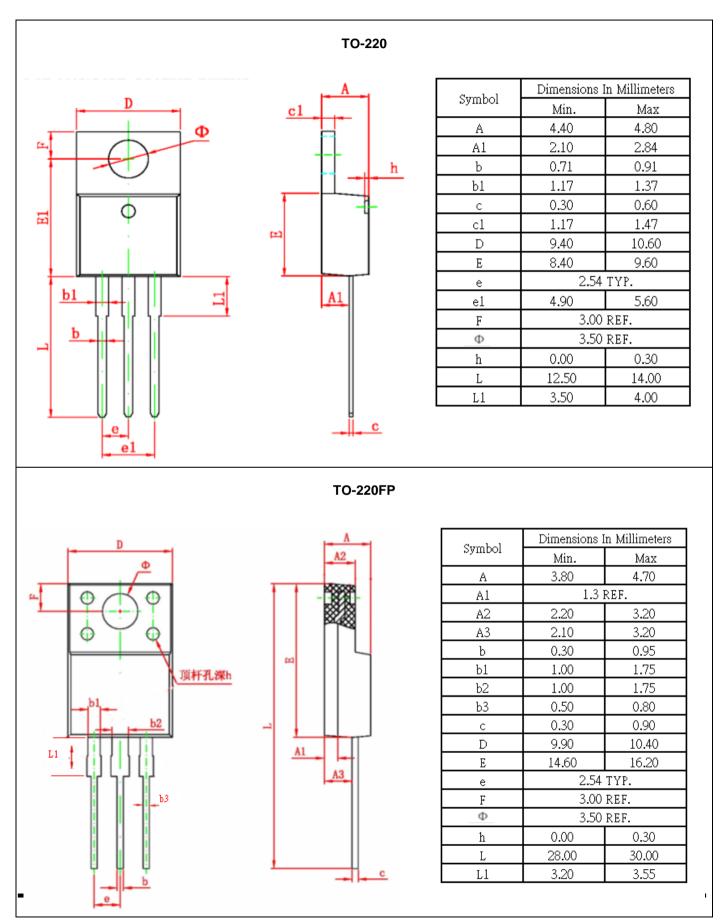
Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage







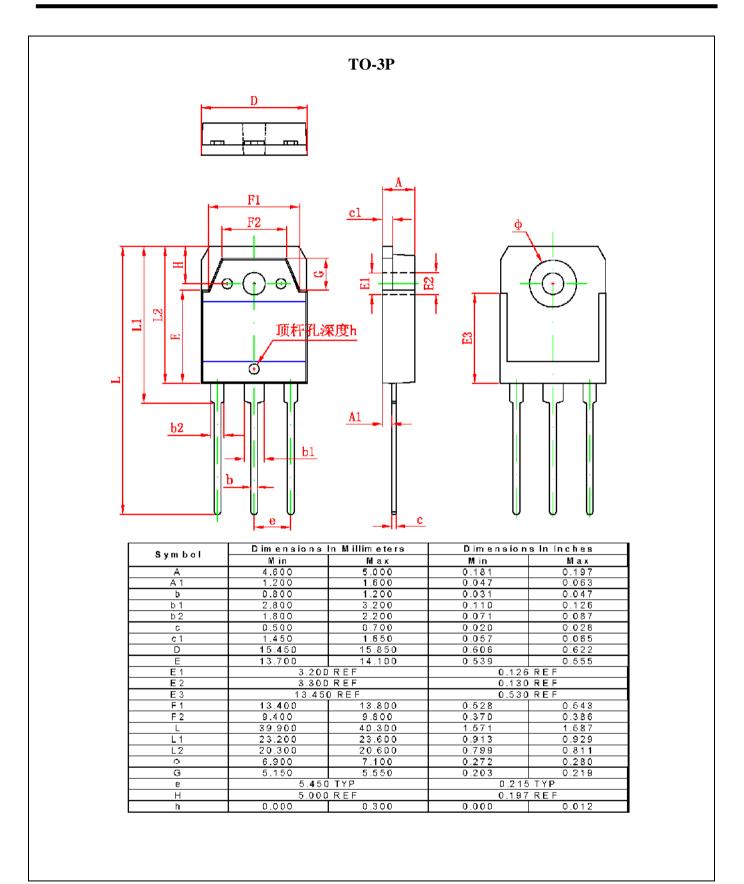
PACKAGE DIMENSION







GP28S50 Power Field Effect Transistor







GP28S50 Power Field Effect Transistor

TO-247 顶杆孔深h <u>c1</u> 묊 뮲 cų **A1** b2 b1 b c **Dimensions In Millimeters Dimensions In Inches** Symbol Min Max Min Max 5.150 0.191 0.200 А 4.850 2.200 2.600 0.087 A1 0.102 1.000 1.400 0.039 0.055 b 3.200 2.800 0.110 0.126 b1 b2 1.800 2.200 0.071 0.087 0.500 0.700 0.020 0.028 С 1.900 2.100 0.075 0.083 c1 15.450 15.750 D 0.608 0.620 E1 3.500 REF 0.138 REF 3.600 REF E2 0.142 REF 40.900 41.300 1.610 L 1.626 0.976 L1 24.800 25.100 0.988 L2 20.300 20.600 0.799 0.811 7.100 7.300 0.280 0.287 φ 5.450 TYP 0.215 TYP е 5.980 REF Н 0.235 REF 0.000 0.300 0.000 0.012 h



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